1 09/347,690

PATENT

plicant:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE Manpreet S. Khaira et al.

Examiner: Dwin M Craig

Serial No.:

09/347,690

Group Art Unit: 2123

Filed:

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Docket No.: 884.107US1

Title:

LOGIC VERIFICATION IN LARGE SYSTEMS

Assignee:

Intel Corporation

Customer No: 21186

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.1 RECEIVED

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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Technology Center 2100

This communication responds to the Office Action mailed on May 20, 2004. Please amend the above-identified patent Application as follows, and consider the appended remarks.